30V PNP LOW SATURATION TRANSISTOR IN SOT223

SUMMARY

 BV_{CEO} = -30V : R_{SAT} = 31m Ω ; I_{C} = -5.5A

DESCRIPTION

Packaged in the SOT223 outline this new low saturation 30V PNP transistor offers extremely low on state losses making it ideal for use in DC-DC circuits and various driving and power management functions.

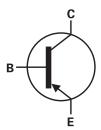


FEATURES SOT223

- 5.5 Amps continuous current
- Up to 20 amps peak current
- Very low saturation voltages
- Exceptional gain linearity down to 10mA

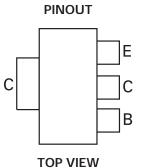
APPLICATIONS

- DC DC converters
- MOSFET gate drivers
- · Charging circuits
- Power switches
- Motor control



ORDERING INFORMATION

DEVICE	REEL SIZE	TAPE WIDTH	QUANTITY PER REEL
ZXTP2008GTA	7″	12mm	1,000 units
ZXTP2008GTC	13"	embossed	4,000 units



DEVICE MARKING

ZXTP 2008

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	LIMIT	UNIT
Collector-base voltage	BV _{CBO}	-50	V
Collector-emitter voltage	BV _{CEO}	-30	V
Emitter-base voltage	BV _{EBO}	-7	V
Continuous collector current ^(a)	I _C	-5.5	А
Peak pulse current	I _{CM}	-20	А
Power dissipation at T _A =25°C ^(a)	P _D	3.0	W
Linear derating factor		24	mW/°C
Power dissipation at T _A =25°C (b)	P _D	1.6	W
Linear derating factor		12.8	mW/°C
Operating and storage temperature range	T _j , T _{stg}	-55 to 150	°C

THERMAL RESISTANCE

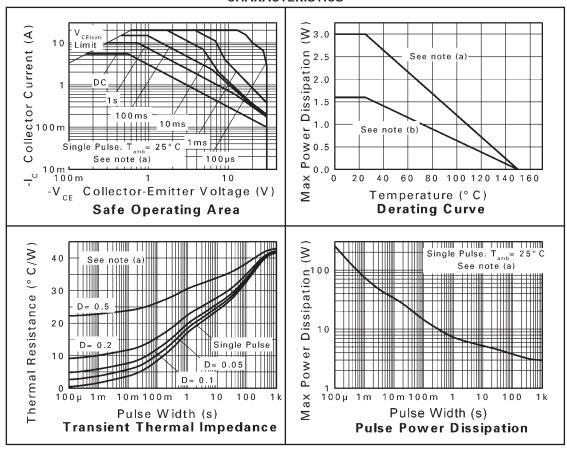
PARAMETER	SYMBOL	VALUE	UNIT
Junction to ambient (a)	$R_{\Theta JA}$	42	°C/W
Junction to ambient ^(b)	$R_{\Theta JA}$	78	°C/W

NOTES

(a) For a device surface mounted on 52mm x 52mm x 1.6mm FR4 PCB with high coverage of single sided 2oz copper, in still air conditions.

(b) For a device surface mounted on 25mm x 25mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.

CHARACTERISTICS

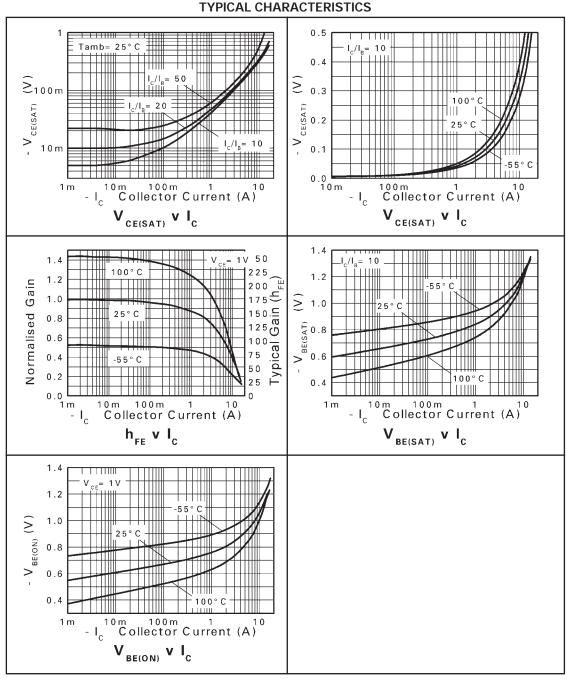


ELECTRICAL CHARACTERISTICS (at T_{amb} = 25°C unless otherwise stated)

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS
Collector-base breakdown voltage	BV _{CBO}	-50	-70		V	I _C = -100μA
Collector-emitter breakdown voltage	BV _{CER}	-50	-70		V	$I_C = -1\mu A$, RB < $1k\Omega$
Collector-emitter breakdown voltage	BV _{CEO}	-30	-40		V	I _C = -10mA *
Emitter-base breakdown voltage	BV _{EBO}	-7.0	-8.0		V	I _E = -100μA
Collector cut-off current	I _{CBO}		<1	-20	nA	V _{CB} = -40V
				-0.5	μΑ	V _{CB} = -40V, T _{amb} = 100°C
Collector cut-off current	I _{CER}		<1	-20	nA	V _{CB} = -40V
	$R < 1k\Omega$			-0.5	μΑ	V _{CB} = -40V, T _{amb} = 100°C
Emitter cut-off current	I _{EBO}		<1	-10	nA	V _{EB} = -6V
Collector-emitter saturation voltage	V _{CE(SAT)}		-30	-45	mV	$I_C = -0.5A$, $I_B = -20mA$ *
			-40	-60	mV	I _C = -1A, I _B = -100mA *
			-60	-85	mV	$I_C = -1A$, $I_B = -20mA$ *
			-70	-90	mV	$I_C = -2A$, $I_B = -200mA$ *
			-170	-210	mV	$I_C = -5.5A$, $I_B = -500mA$ *
Base-emitter saturation voltage	V _{BE(SAT)}		-1030	-1130	mV	I _C = -5.5A, I _B = -500mA *
Base-emitter turn-on voltage	V _{BE(ON)}		-900	-1000	mV	I _C = -5.5A, V _{CE} = -1V *
Static forward current transfer ratio	h _{FE}	100	225			I _C = -10mA, V _{CE} = -1V *
		100	200	300		I _C = -1A, V _{CE} = -1V *
		70	145			$I_C = -5A$, $V_{CE} = -1V$ *
		10	20			I _C = -20A, V _{CE} = -1V *
Transition frequency	f _T		110			I _C = -100mA, V _{CE} = -10V
						f = 50MHz
Output capacitance	СОВО		83		pF	V _{CB} = -10V, f = 1MHz *
Switching times	t _{ON}		43		ns	I _C = -1A, V _{CC} = -10V,
	t _{OFF}		230			$I_{B1} = I_{B2} = -100 \text{mA}$

NOTES

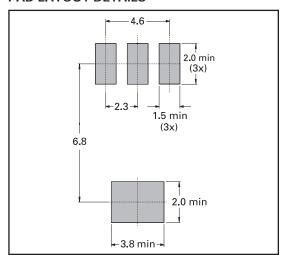
^{*} Measured under pulsed conditions. Pulse width $\leq 300 \mu s;$ duty cycle $\leq\!2\%.$



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PACKAGE OUTLINE

PAD LAYOUT DETAILS



Controlling dimensions are in millimeters. Approximate conversions are given in inches

PACKAGE DIMENSIONS

DIM	Millin	neters	Inc	hes	DIM	Millimeters		Inches	
DIIVI	Min	Max	Min	Max	Diivi	Min	Max	Min	Max
Α	-	1.80	-	0.071	е	2.30 BSC		0.0905 BSC	
A1	0.02	0.10	0.0008	0.004	e1	4.60 BSC		0.181 BSC	
b	0.66	0.84	0.026	0.033	Е	6.70	7.30	0.264	0.287
b2	2.90	3.10	0.114	0.122	E1	3.30	3.70	0.130	0.146
С	0.23	0.33	0.009	0.013	L	0.90	-	0.355	-
D	6.30	6.70	0.248	0.264	-	-		-	-

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